

AMENDMENTS TO THE CLAIMS:

Please amend the claims as follows, substituting any amended claim(s) for the corresponding pending claim(s):

- 1 1. (Currently Amended) ~~For use in association with~~ A system for the on-line insertion
2 of a line replaceable unit into a backplane of an item of electronic equipment, wherein said
3 backplane comprises a common control bus and one or more of one or more additional control buses,
4 one or more operations buses, and one or more traffic buses, ~~a system for the on-line insertion of a~~
5 ~~line replaceable unit into said backplane,~~ said system comprising:
6 a primary master controller inserted into said backplane, said primary master
7 controller capable of communicating via said common control bus of said backplane with said line
8 replaceable unit when said line replaceable unit is inserted into said backplane;
9 wherein said line replaceable unit is partially powered but does not have full access
10 to all buses in said backplane or access to full power when said line replaceable unit is first inserted
11 into said backplane; and
12 wherein said primary master controller is capable of causing said line replaceable unit
13 to have full access to remaining buses in said backplane and full power.

1 2. (Original) The system as set forth in Claim 1 wherein said primary master controller
2 is capable of determining whether said line replaceable unit that is inserted into said backplane is
3 ready for operation.

1 3. (Original) The system as set forth in Claim 2 wherein said primary master controller
2 is capable of downloading at least one software update to said line replaceable unit to cause said line
3 replaceable unit to be ready for operation.

1 4. (Original) The system as set forth in Claim 1 wherein said primary master controller
2 is capable of causing said line replaceable unit to have access to full power in said backplane.

1 5. (Original) The system as set forth in Claim 1 wherein said primary master controller
2 comprises an interface control processor card, and wherein said line replaceable unit comprises a
3 circuit board card.

1 6. (Original) The system as set forth in Claim 1 further comprising a secondary master
2 controller inserted into said backplane, said secondary master controller capable of performing the
3 functions of said primary master controller when said primary master controller is not operating.

1 7. (Original) The system as set forth in Claim 1 wherein said primary master controller
2 is capable of disconnecting full access of said line replaceable unit to said backplane after said
3 primary master controller has caused said line replaceable unit to have full access to said backplane.

1 8. (Original) The system as set forth in Claim 7 wherein said primary master controller
2 is capable of disconnecting full access of said line replaceable unit to said backplane by disabling
3 power to all but common control power sections of said line replaceable unit.

1 9. (Original) The system as set forth in Claim 1 further comprising:
2 a circuit board card capable of being inserted into said backplane, said circuit board
3 card comprising a hot swap power / in rush controller for regulating power to said circuit board card
4 when said circuit board card is first inserted into said backplane;
5 a card processor on said circuit board card, wherein said card processor is capable of
6 determining whether said circuit board card is located in a primary master controller slot of said
7 backplane, in which case said circuit board card operates as a primary master controller; and
8 wherein said card processor is capable of determining whether said circuit board card
9 is located in a secondary master controller slot of said backplane, in which case said circuit board
10 card operates as a secondary master controller when said primary master controller is not operating.

1 10. (Original) The system as set forth in Claim 1 further comprising:

2 a circuit board card capable of being inserted into said backplane, said circuit board
3 card comprising a hot swap power / in rush controller for regulating power to said circuit board card
4 when said circuit board card is first inserted into said backplane; and

5 a card processor on said circuit board card, said card processor capable of determining
6 whether said circuit board card is located in a non-master controller slot of said backplane, in which
7 case said circuit board card waits for said primary master controller to cause said circuit board card
8 to have full access to said backplane.

1 11. (Currently Amended) For use in association with a backplane of an item of electronic
2 equipment wherein said backplane comprises a common control bus, one or more additional control
3 buses, one or more operations buses, and one or more traffic buses, a method for the on-line insertion
4 of a line replaceable unit into said backplane, said method comprising ~~the steps of:~~

5 inserting a primary master controller into said backplane;

6 inserting said line replaceable unit into said backplane so that said line replaceable
7 unit is partially powered but does not have full access to all buses in said backplane or access to full
8 power when said line replaceable unit is first inserted into said backplane; and

9 controlling the access of said line replaceable unit to remaining buses in said
10 backplane and full power with said primary master controller.

1 12. (Original) The method as set forth in Claim 11 further comprising the step of:
2 determining in said primary master controller whether said line replaceable unit that
3 is inserted into said backplane is ready for operation.

1 13. (Original) The method as set forth in Claim 12 further comprising the step of:
2 downloading to said line replaceable unit from said primary master controller at least
3 one software update to cause said line replaceable unit to be ready for operation.

1 14. (Original) The method as set forth in Claim 11 further comprising the step of:
2 controlling the access of said line replaceable unit to full power in said backplane
3 with said primary master controller.

1 15. (Original) The method as set forth in Claim 11 wherein said primary master
2 controller comprises an interface control processor card, and wherein said line replaceable unit
3 comprises a circuit board card.

1 16. (Original) The method as set forth in Claim 11 further comprising the step of:
2 using a secondary master controller to perform the functions of said primary master
3 controller when said primary master controller is not operating.

1 17. (Original) The method as set forth in Claim 11 further comprising the step of:
2 causing said primary master controller to disconnect full access of said line replaceable unit
3 to said backplane by disabling power to all but common control power sections of said line
4 replaceable unit.

1 18. (Currently Amended) For use in association with a backplane of an item of electronic
2 equipment wherein said backplane comprises a common control bus, one or more additional control
3 buses, one or more operations buses, and one or more traffic buses, a method for the on-line insertion
4 of a line replaceable unit into said backplane, said method comprising ~~the steps of:~~
5 inserting a circuit board card into said backplane;
6 providing a controlled power ramp up to said circuit board card;
7 determining whether a voltage rail has failed;
8 starting a reset timer;
9 running a power on self test on said circuit board card;
10 determining whether said circuit board card passed said power on self test; and
11 activating a common control bus.

1 19. (Original) The method as set forth in Claim 18, said method further comprising the
2 steps of:

3 determining whether said circuit board card is in a master slot of said backplane;

4 determining whether said circuit board card is a primary master controller if said
5 circuit board card is in a master slot of said backplane;

6 operating said circuit board card as a primary master controller if said circuit board
7 card is a primary master controller;

8 determining whether said circuit board card is a secondary master controller if said
9 circuit board card is in a master slot of said backplane; and

10 operating said circuit board card as a secondary master controller if said circuit board
11 card is a secondary master controller.

1 20. (Original) The method as set forth in Claim 18, said method further comprising the
2 steps of:
3 determining that said circuit board card is not in a master slot of said backplane;
4 waiting for a primary master controller to interrogate said circuit board card;
5 configuring said circuit board card with said primary master controller;
6 activating said circuit board card with said primary master controller; and
7 operating said circuit board card in normal operation.